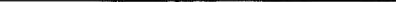


Examiner Signature		Date Considered 4/11/04
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Substitute for Form 1449A/PTO (Modified)

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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JUN 06 2002

of 2

Complete if Known

Application Number	09/672,602
Filing Date	9/29/00
First Named Inventor:	Carl M. Ellison
Group Art Unit	2177
Examiner Name	

Sheet 2

OTHER ART – NO PATENT | LITERATURE DOCUMENTS

Examiner
Signature

Date
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Sheet 1

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Attorney Docket Number

09/672.602

9/29/00

Carl M. Ellison

2177

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042390 P8629X

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FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered 4/11/24	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2

OTHER ARTS & TRADEMARKS | PATENT LITERATURE DOCUMENTS

Complete if Known	
Application Number	09/672,602
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First Named Inventor:	Carl M. Ellison
Group Art Unit	2177
Examiner Name	
Attorney Docket Number	042390.P8629X

OTHER ART & TRADEMAI
RE-PATENT LITERATURE DOCUMENTS

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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1 & TRADEMARKS

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of 2

Application Number	09/672,602
Filing Date	9/29/00
First Named Inventor:	Ellison
Group Art Unit	2177
Examiner Name	

Attorney Docket Number 042390.P8629X

Sheet 1

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (If known)			
WA		4,278,837		Best	7/14/81	
WA		4,521,852		Guttag	6/4/85	
WA		5,022,077		Bealkowski et al.	6/4/91	
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WA		5,473,692		Davis	12/5/95	
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		Office ³	Number ⁴	Kind Code ⁵ (If known)			
WA		EP	1146715		European Patent Application	10/17/01	
WA		WO	0062232		PCT	10/19/00	
WA		WO	9905600		PCT	2/4/99	
WA		WO	9957863		PCT	11/11/99	
WA		WO	9844402		PCT	10/8/98	

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Date Considered

4/16/04

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Examiner Signature		Date Considered 4/14/13	
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Complete if Known**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 1 of 2

Application Number	09/672,602
Filing Date	September 29, 2000
First Named Inventor:	Carl M. Ellison
Group Art Unit	2177
Examiner Name	Unassigned

Attorney Docket Number 042390P8629X

OTHER ART – NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		GOLDBERG, R., "Survey of virtual machine research," IEEE Computer Magazine 7(6), pp. 34-45, 1974.	
		GUM, P.H., "System/370 Extended Architecture: Facilities for Virtual Machines," IBM J. Research Development, Vol 27, Number 6, pp. 530-544, November 1983	
		ROSENBLUM, M. "Vmware's Virtual Platform: A Virtual Machine Monitor for Commodity PCs," Proceedings of the 11th Hotchips Conference, pp. 185-196, August 1999.	
		LAWTON, K., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques," http://www.plex86.org/research/paper.txt ; 11/29/1999; pp. 1-31.	
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			NOV 29 2002
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Examiner Signature	Date Considered
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

of 1

Application Number	09/672,602
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Group Art Unit	2177
Examiner Name	Unassigned

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		Number	Kind Code ² (If known)			
KA		6,505,279		Phillips et al.	01-07-2003	
KA		5,079,737		Hackbarth	01-07-1992	RECEIVED
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KA		5,872,994		Akiyama et al.	02-16-1999	
KA		6,301,646		Hostetter	10-09-2001	
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		Office ³	Number ⁴ Kind Code ⁵ (If known)				
KA		WO	97/29567	Fortress U & T Ltd.	08-14-1997		
KA		WO	01/75565	Intel Corporation	10-11-2001		
KA		WO	01/75595	Intel Corporation	10-11-2001		
KA		WO	01/27723	Hewlett-Packard Company	04-19-2001		
KA		WO	01/27821	Hewlett-Packard Company	04-19-2001		

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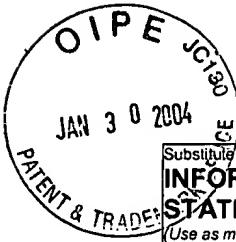
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Examiner Signature		Date Considered 4/16/07	
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Substitute for form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Complete if Known

Application Number	09/672602
Filing Date	September 29, 2000
First Named Inventor	Ellison, Carl
Group Art Unit	Unknown
Examiner Name	Unknown

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Sheet 1 of 2

Attorney Docket No: 42P08629X

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FOREIGN PATENT DOCUMENTS

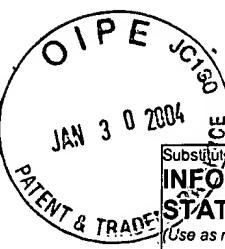
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	DE-DE4217444	12/03/1992	Toyohisa, Imada , et al.			
	EP-EP0473913	03/11/1992	Farrell, Joel A.			
	JP-JP2000076139	03/14/2000	Tanno, Masaaki , et al.			
	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.			
	WO-WO02086684	10/31/2002	Proudler, Graeme J.			
	WO-WO0217555	02/28/2002	Ford, Warwick			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

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		BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995),Chapter 3	
		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", 7th Annual IEEE Symposium, FCCM '99 Proceedings, XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA, (4/21/1999),209-221	
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		KARGER, PAUL A., et al., "A VMM Security Kernal for the VAX Architecture", Proceedings of the Symposium on Research in Security and Privacy, XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	
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		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", IEEE AES Systems Magazine, XP002190614,(March 1999),	
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		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium, XP002247347, Denver, Colorado, (8/14/00),1-17	

EXAMINER

DATE CONSIDERED



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;">Application Number</td> <td style="width: 60%;">09/672602</td> </tr> <tr> <td>Filing Date</td> <td>September 29, 2000</td> </tr> <tr> <td>First Named Inventor</td> <td>Ellison, Carl</td> </tr> <tr> <td>Group Art Unit</td> <td>Unknown</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> </table> <div style="display: flex; justify-content: space-between; align-items: center; margin-top: 10px;"> Sheet 2 of 2 Attorney Docket No: 42P08629X RECEIVED FEB 03 2004 Technology Center 2100 </div>		Application Number	09/672602	Filing Date	September 29, 2000	First Named Inventor	Ellison, Carl	Group Art Unit	Unknown	Examiner Name	Unknown
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		SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", <u>Proceedings of the IEEE International Symposium on Industrial Electronics</u> , XP002190615,(July 1999),43-48	
		SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001)</u> ,	

EXAMINER

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